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Homepage for Digital Integrated Circuits

CMOS Digital Integrated Circuits: Analysis and Design. Second Edition. Postscript files are created for US Letter size paper and printed in reverse order. Notice that gzip compressed files are noticeably smaller in size and therefore easier to download. Aldus Pagemaker version 4.2 files are also provided.

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Digital Integrated Circuits AND VLSI Fundamentals Course: ESE570 Units: 1.0 CU Term: Spring 2020 When: TR 1:30-3pm Where: Virtual Session Instructor: Tania Khanna (Levine 262) (seas: taniak) (office hours: WF 2:30-4pm and by appointment) TA: Yuanlong Xiao (seas: ylxiao) Office hours: TuTh 3-5pm in Virtual Session TA: AJ Geers (seas: ageers) Office hours: Th 5:30-7:30pm in Virtual Session

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Digital Integrated Circuits: A Design Perspective J. Rabaey, A. Chandrakasan, and B. Nikolic 2nd edition Web Page. References: CMOS Digital Integrated Circuits: Analysis and Design S-M. Kang and Y. Leblebici 3rd edition . CMOS VLSI Design - A Circuits and Systems Perspective N. H. Weste and D. Harris 4th edition . Midterm: Mon 10/31/11 in class.

EEEC116 - Fall 2011

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